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Docket No.: H6808.0004/P004

Confirmation No.: 5346

Group Art Unit: 2881

Examiner: P. Johnston

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Atsushi Takane, et al

Application No.: 10/082,286

Filed: February 26, 2002

For: SEMICONDUCTOR INSPECTION

SYSTEM

SUPPLEMENTAL SUBMISSION OF FORMAL DRAWINGS

Commissioner for Patents Washington, DC 20231

Dear Sir:

In addition to the Submission of Formal Drawings filed in the above-identified application on February 13, 2003, applicants submit herewith one set (three sheets, Figures 2, 4 and 5) of formal drawings for filing in the above-identified patent application. Kindly substitute the enclosed formal drawings for the formal drawings submitted on February 13, 2003.

Dated: March 28, 2003

Respectfully submitted,

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